

FIG. 1
(prior art)

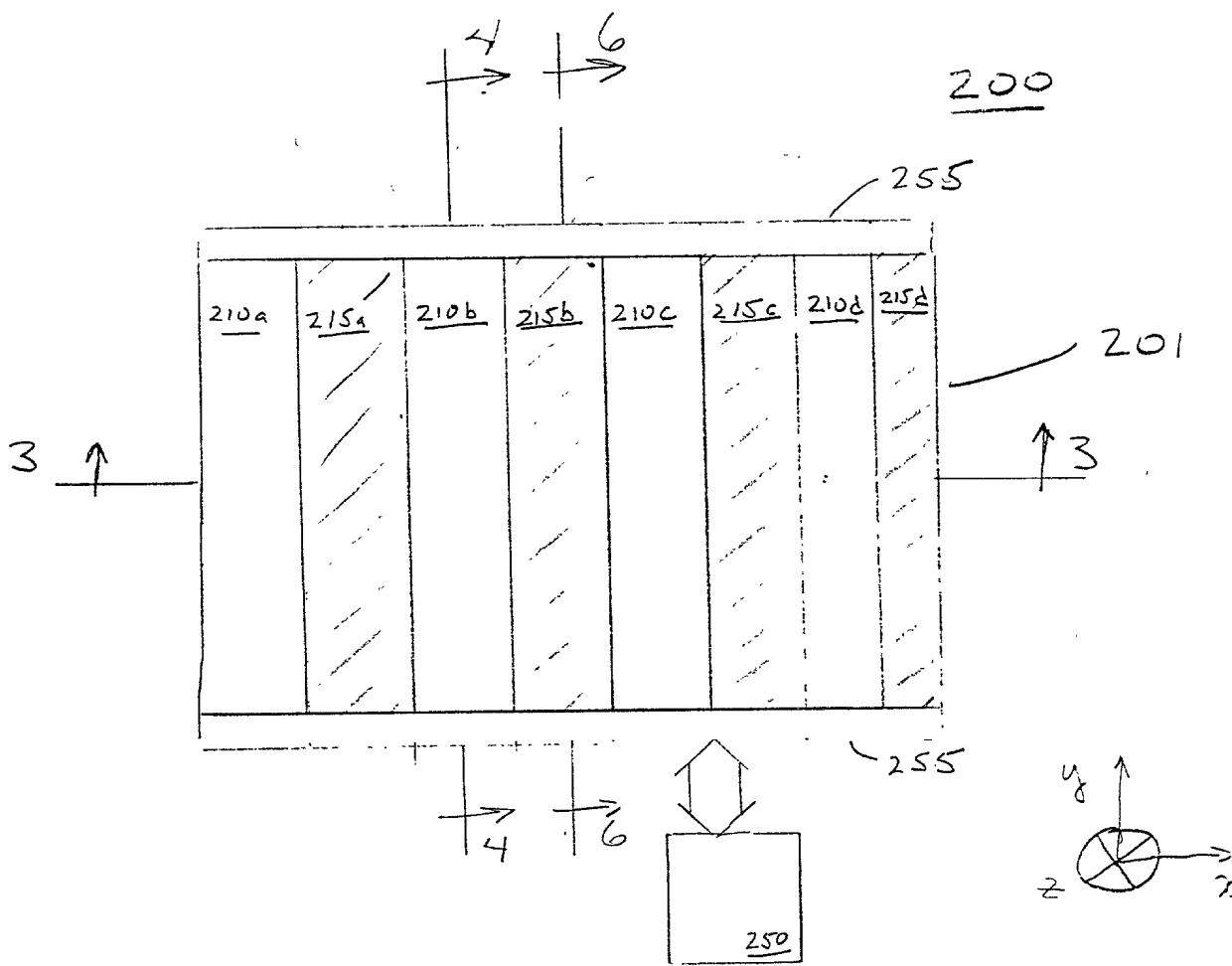
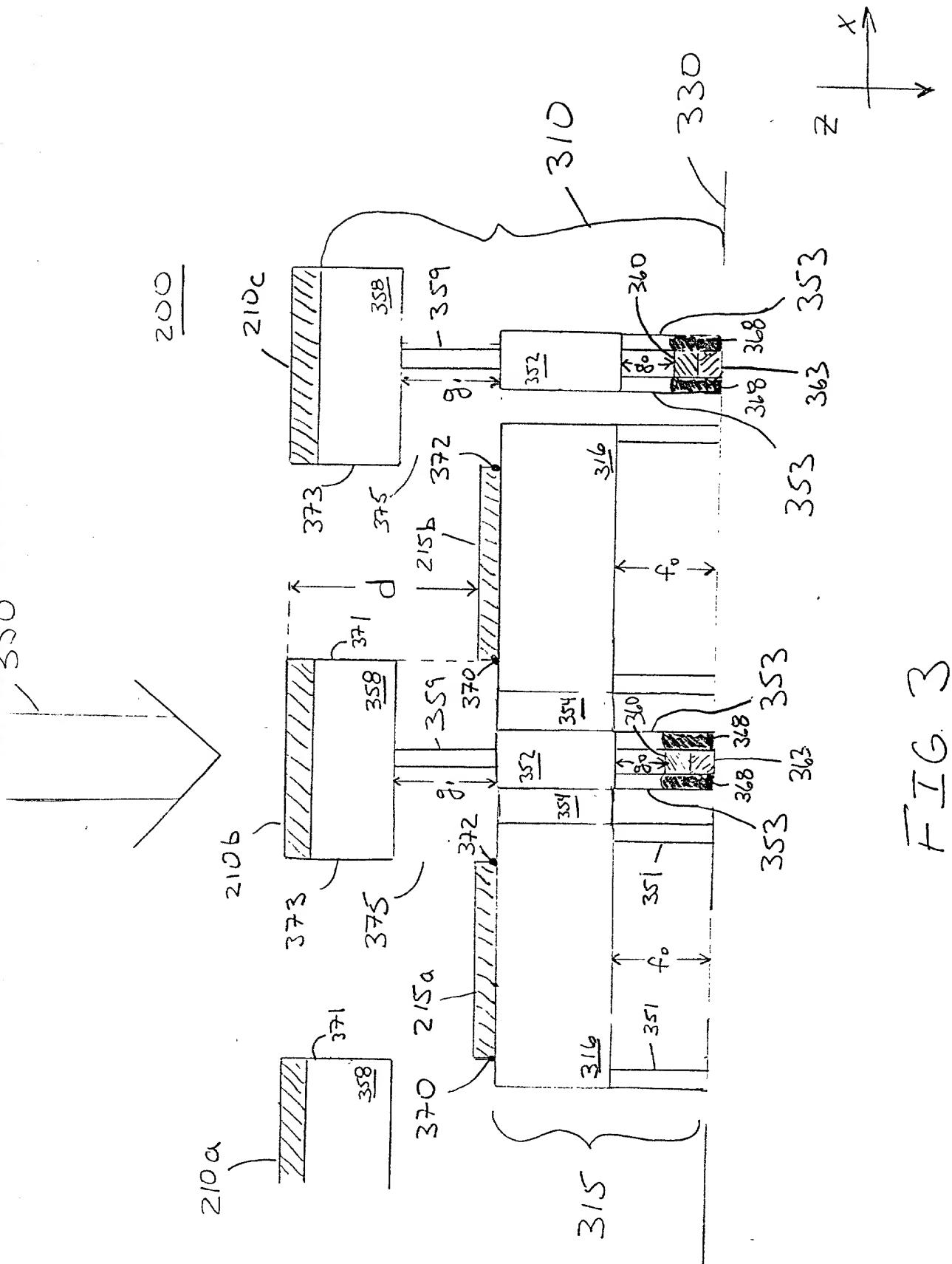


FIG 2



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Title: Acceptable Diffraction Optical Processor
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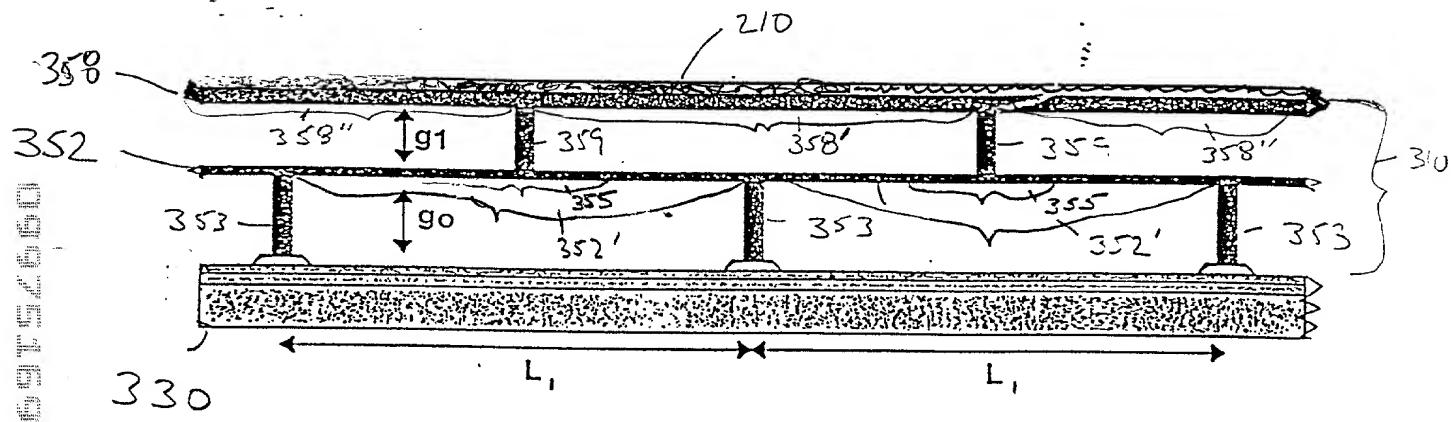


FIG 4

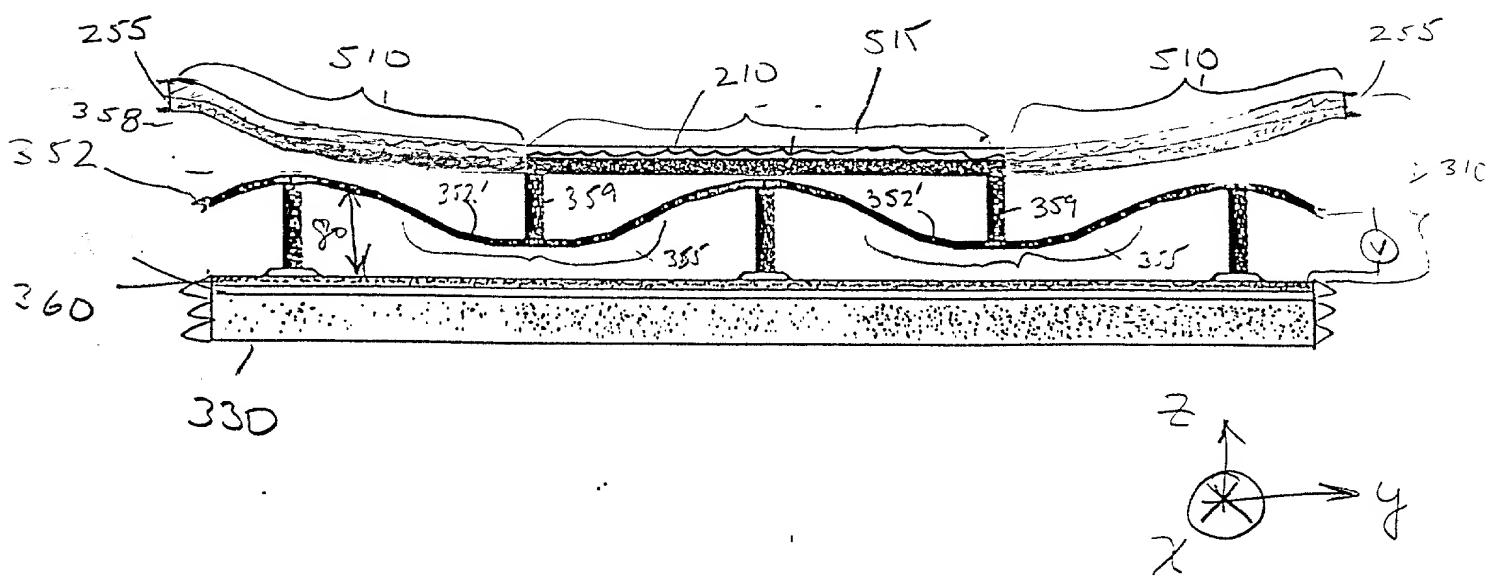


FIG 5

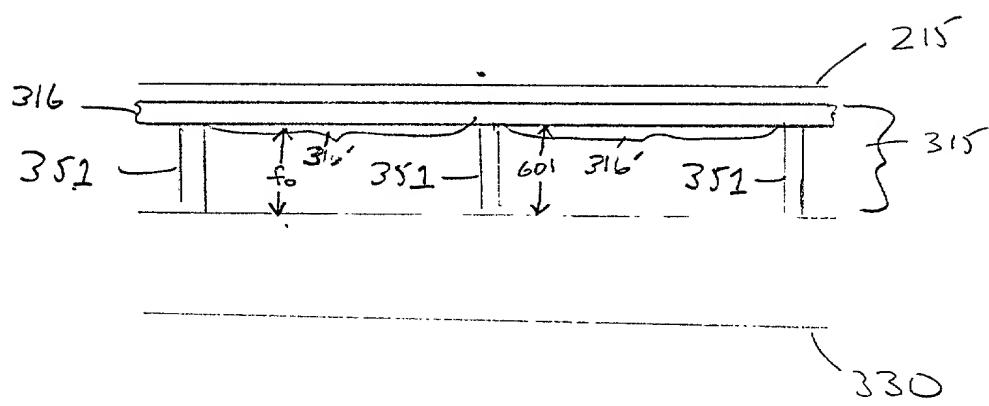


FIG 6

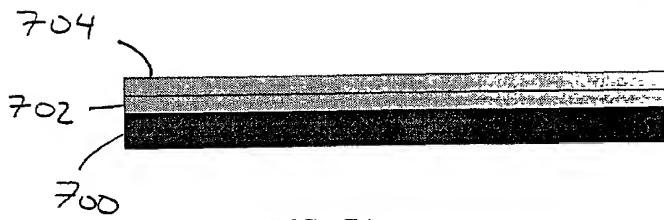


FIG. 7A

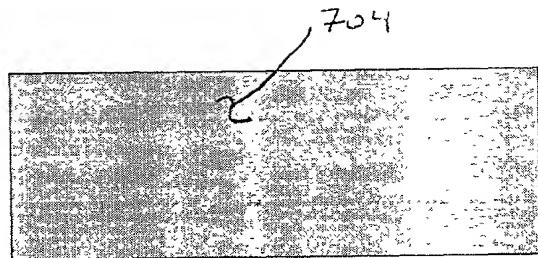


FIG. 8A

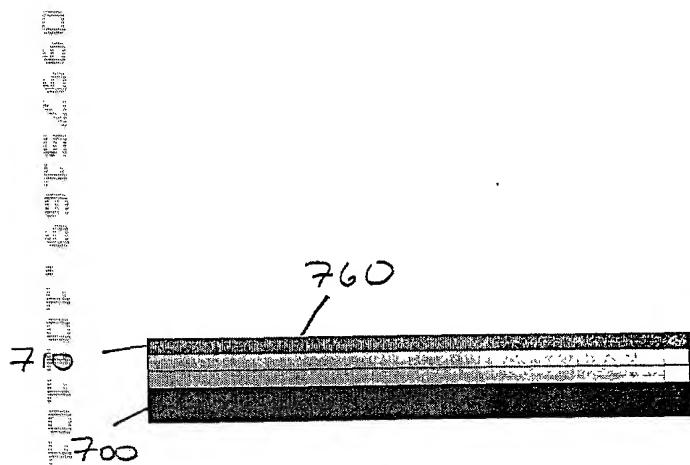


FIG. 7B

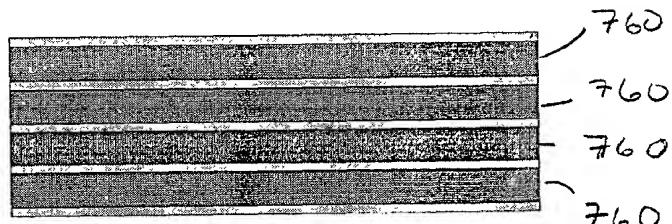


FIG. 8B

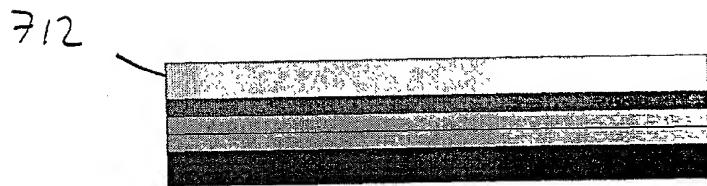


FIG. 7C

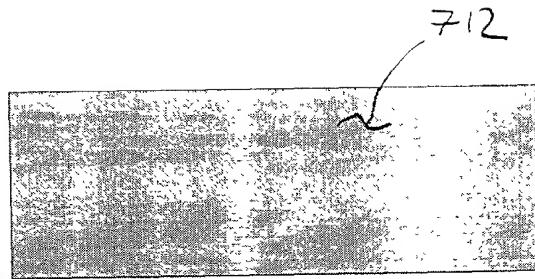


FIG. 8C

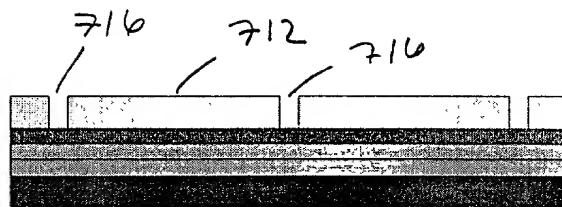


FIG. 7D

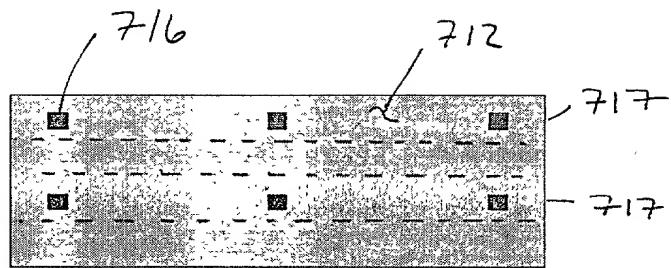


FIG. 8D

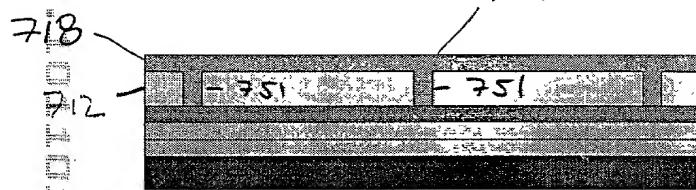


FIG. 7E

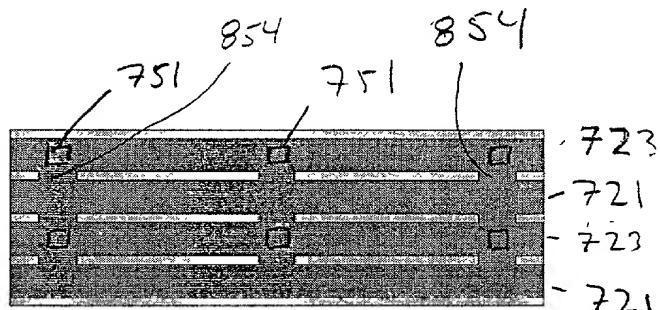


FIG. 8E

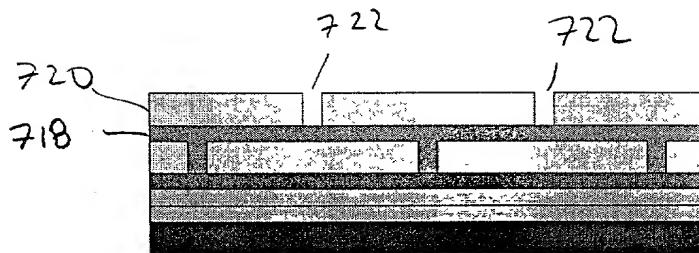


FIG. 7F

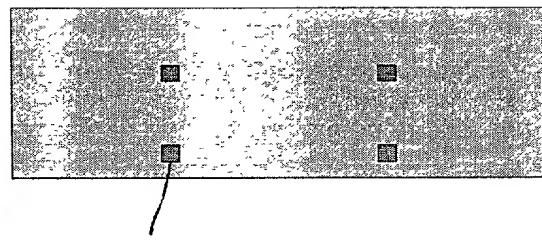


FIG. 8F

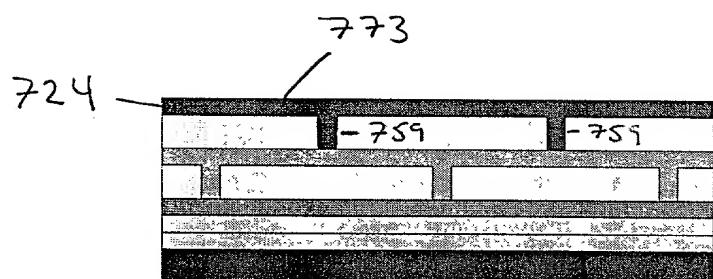


FIG. 7G

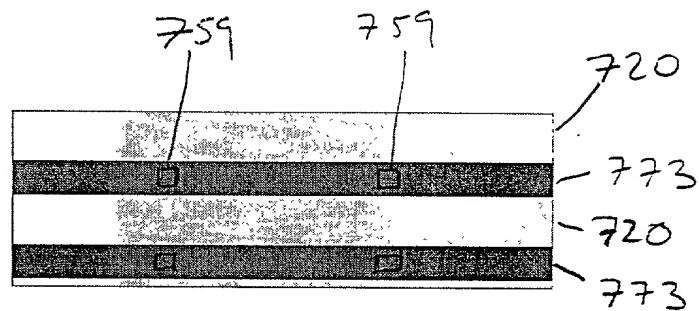


FIG. 8G

TOP SURFACE

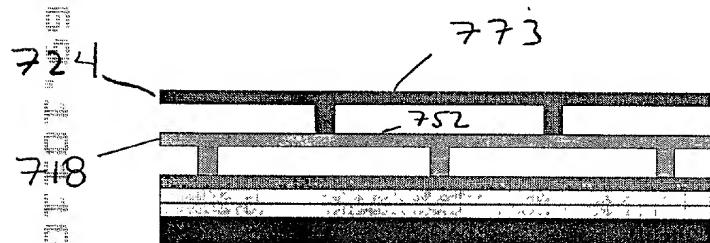


FIG. 7H

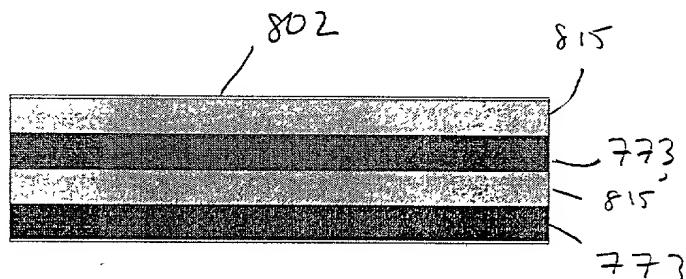


FIG. 8H

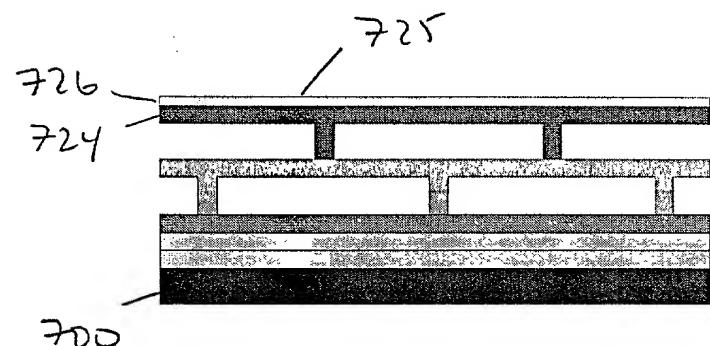


FIG. 7I

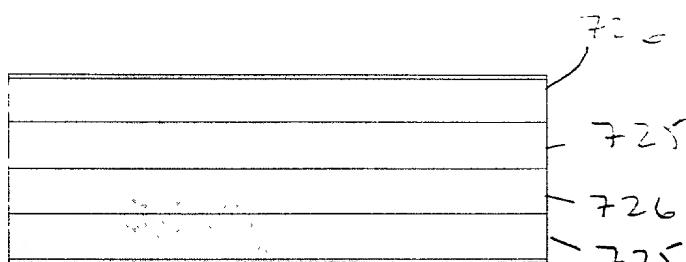


FIG. 8I

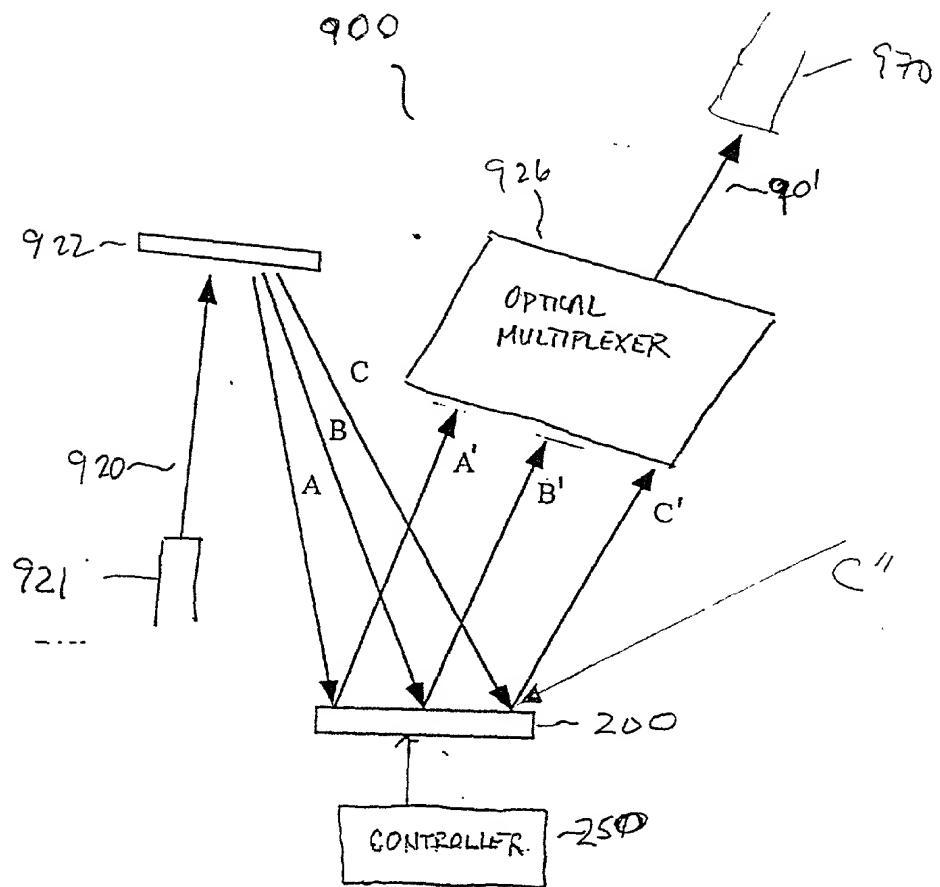


FIG. 9.

1000

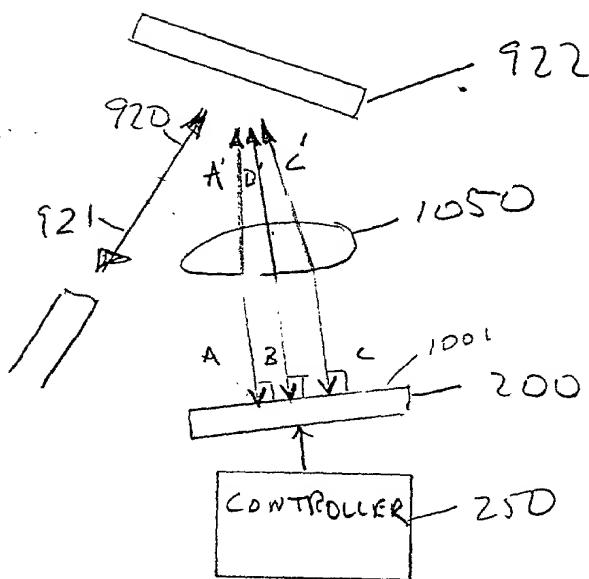


FIG. 10